

WHAT IS CLAIMED IS:

1. A liquid crystal display device comprising:
 - first and second substrates each comprising an active region and a dummy region;
 - a main sealant pattern arranged between the first and second substrates and at a periphery of the active regions of the first and second substrates;
 - a dummy sealant pattern arranged between the first and second substrates and within the dummy regions of the first and second substrates;
 - a first column spacer arranged between the first and second substrates and within the dummy regions of the first and second substrates; and
 - a liquid crystal layer between the first and second substrates and within the active regions of the first and second substrates.
2. The device as claimed in claim 1, further comprising a second column spacer arranged between the first and second substrates within the active region.
3. The device as claimed in claim 1, further comprising a recessed structure within the dummy region on the first substrate.
4. The device as claimed in claim 3, wherein at least a portion of the first column spacer is formed within the recessed structure.
5. A method for fabricating a liquid crystal display device, comprising:
 - providing first and second substrates each comprising a dummy region and an active

region;

forming first column spacers within the dummy regions of one of the first and second substrates;

forming a main sealant pattern at a periphery of the active region and forming a dummy sealant pattern within the dummy region of one of the first and second substrates;

dispensing liquid crystal material onto the active region of one of the first and second substrates; and

bonding the first and second substrates.

6. The method as claimed in claim 5, further comprising forming second column spacers within active regions of one of the first and second substrates

7. The method as claimed in claim 6, wherein

the main sealant pattern and the dummy sealant pattern are formed on the first substrate and the second column spacers are formed on the second substrate.

8. The method as claimed in claim 5, wherein

the main sealant pattern and the dummy sealant pattern are formed on the first substrate and the first column spacers are formed on the second substrate.

9. The method as claimed in claim 5, further comprising forming a recess within the

dummy region of the first substrate.

10. The method as claimed in claim 9, further comprising forming at least a portion of the dummy sealant pattern within the recess.

11. A method for fabricating a liquid crystal display device, comprising:
providing first and second substrates having a dummy region and an active region;
forming a thin film transistor array within the active region on the first substrate, the thin film transistor array comprising a gate line, a data line, and source/drain electrodes;
forming a protection film on the first substrate;
forming a contact hole within the protection film to expose the drain electrode;
forming a pixel electrode in a pixel region so as to be connected to the drain electrode through the contact hole;
forming a color filter array in the active region of the second substrate;
forming a first column spacer in the dummy region of the second substrate;
forming a main sealant pattern at a periphery of the active region and forming a dummy sealant pattern within the dummy region of one of the first and second substrates;
dispensing liquid crystal onto one of the first and second substrates; and
bonding the first and second substrates.

12. The method as claimed in claim 11, further comprising forming a second column spacer in the active region of the second substrate.

13. The method as claimed in claim 11, further comprising forming a recess in the dummy region of the first substrate.

14. The method as claimed in claim 13, further comprising forming at least a portion of the dummy sealant pattern within the recess.

15. The method as claimed in claim 11, wherein the recess is formed in the protection film.

16. The method as claimed in claim 11, wherein the pixel electrode is a transparent electrode.

17. The method as claimed in claim 11, wherein the pixel electrode is a reflective electrode.

18. The method as claimed in claim 11, further comprising forming a black matrix layer within the active region of the second substrate.

19. The method as claimed in claim 18, further comprising forming the second column spacer directly over a portion of the black matrix layer.